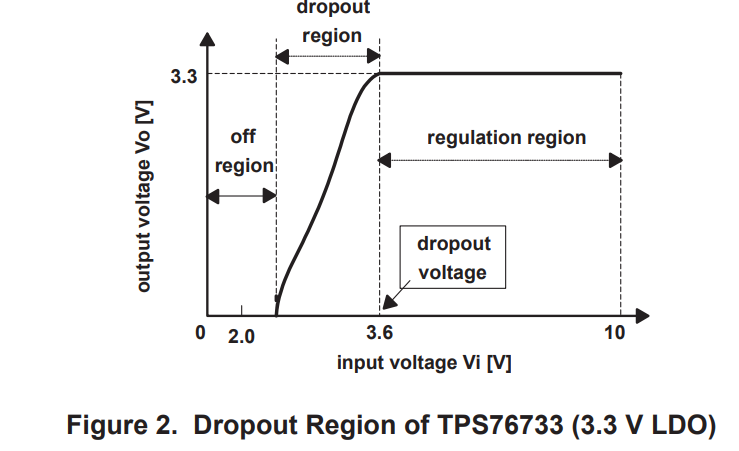
**Terms and Definitions**

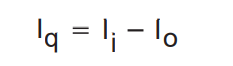
**Dropout Voltage** Minimum voltage drop required across the regulator to maintain output voltage regulation .

For example, Figure 2 shows the input/output characteristics of the TPS76733 3.3-V LDO regulator.,The range of the dropout region is between approximately 2-V and 3.65V input voltage. Below this, the device is nonfunctional. **Low dropout voltage is necessary to maximize the regulator efficiency**



**Quiescent Current**

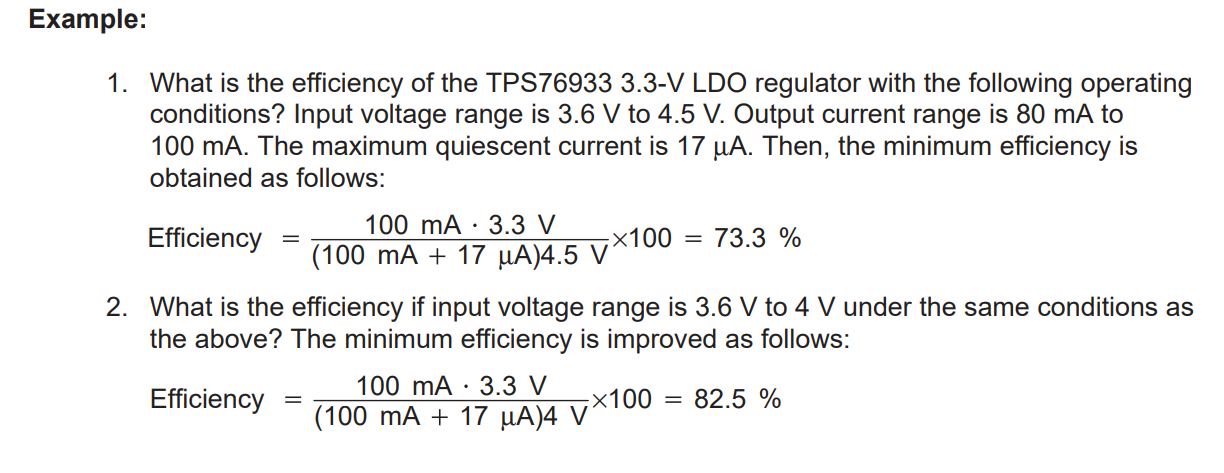
Quiescent, or ground current, is the difference between input and output currents. **Low quiescent current is necessary to maximize the current efficiency**



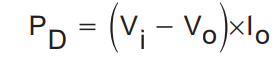
**Efficiency**

The efficiency of LDO regulators is limited by the quiescent current and input/output voltages as follows.

**To have a high efficiency, drop out voltage and quiescent current must be minimize**



The regulator power dissipation is calculated



**Io= Iload + Iq(Quiescent current).**

**Source:**https://www.ti.com/lit/an/slva079/slva079.pdf?ts=1600724716205&ref\_url=https%253A%252F%252Fwww.google.com%252F

REGULADORES DE VOLTAJE 3,3V PARA ELEGIR

MIC5205 PRECIO: $1.666 https://www.sigmaelectronica.net/producto/mic5205-3-3/

DATASHEET

https://www.sigmaelectronica.net/manuals/MIC5205-3.3.pdf



